

## AVE240-48S12

**240 Watts**

**Half-brick Converter**

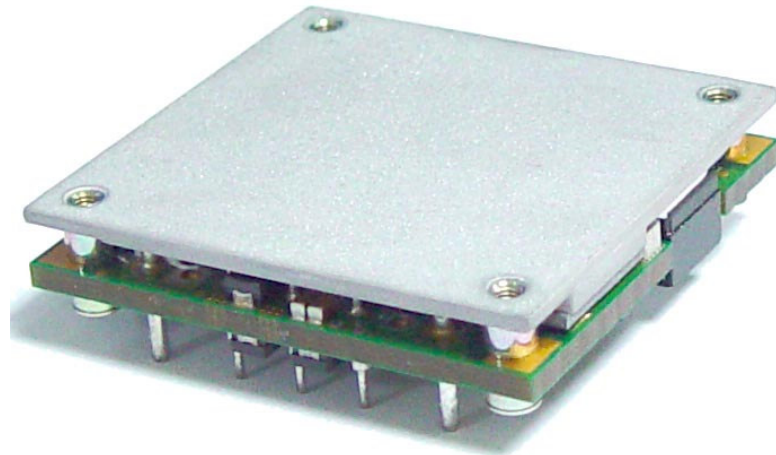
**Total Power:** 240 Watts  
**Input Voltage:** 36 to 75 Vdc  
**# of Outputs:** Single

### Special Features

- Delivering up to 20A output current
- Basic isolation
- Ultra-high efficiency 92% typ. at full load
- Improved thermal performance
- High power density
- Low output noise
- 2:1 wide input voltage of 36-75V
- CNT function
- Remote sense
- Trim function: -20% ~ +10%
- Input under-voltage lockout
- Output over-current protection
- Output over-voltage protection
- Over-temperature protection
- CNT Function optional
- Pins Length optional
- Baseplate optional

### Safety

UL/EN60950  
CSA C22.2



## Product Descriptions

The AVE240-48S12 is a single output DC/DC converter with standard half-brick form factor and output is isolated from input. It delivers up to 20A output current with 12V output, provides CNT and trim functions. Ultra-high 92% efficiency and excellent power density makes it an ideal choice for use in computing and telecommunication applications and can operate over an ambient temperature range of -40 °C ~ +55 °C.

## Applications

Telecom/ Datacom

## Model Numbers

Standard	Output Voltage	Structure	Remote ON/OFF logic	RoHS Status
AVE240-48S12-4	12Vdc	Open frame	Negative	R5
AVE240-48S12B-4	12Vdc	Baseplated	Negative	R5
AVE240-48S12P-4	12Vdc	Open frame	Positive	R5
AVE240-48S12PB-4	12Vdc	Baseplated	Positive	R5

## Ordering information

AVE240	-	48	S	12	P	B	-	4
①		②	③	④	⑤	⑥		⑦

①	Series name	AVE: series name, 240: rated output power 240W
②	Input voltage	48: 36V ~ 75V input range, rated input voltage 48V
③	Output number	S: single output. D:dual output
④	Rated output voltage	12: 12V output
⑤	Remote ON/OFF logic	P: positive logic, open frame; Default: negative logic, open frame
⑥	Baseplate	B: negative & baseplated; PB: positive & baseplated
⑦	Pin length	4: 4.80 mm ± 0.5mm

## Options

None

## Electrical Specifications

### Absolute Maximum Ratings

Stress in excess of those listed in the “Absolute Maximum Ratings” may cause permanent damage to the power supply. These are stress ratings only and functional operation of the unit is not implied at these or any other conditions above those given in the operational sections of this TRN. Exposure to any absolute maximum rated condition for extended periods may adversely affect the power supply’s reliability.

Table 1. Absolute Maximum Ratings:

Parameter	Model	Symbol	Min	Typ	Max	Unit
Input Voltage	Operating -Continuous	All	-	-	80	Vdc
	Non-operating -100mS	All	-	-	100	Vdc
Maximum Output Power	All	$P_{O,max}$	-	-	240	W
Isolation Voltage <sup>1</sup>	Input to outputs	Open frame module	-	-	1500	Vdc
	Input to outputs	Baseplate module	-	-	1500	Vdc
	Input to baseplate	Baseplate module	-	-	1500	Vdc
	Outputs to baseplate	Baseplate module	-	-	500	Vdc
Ambient Operating Temperature (See thermal consideration)	All	$T_A$	-40	25	55	°C
Operating Ambient Temperature	With baseplate (Center of baseplate)	All	100	-	115	°C
	Without baseplate (Near temperature sensor Rt)	All	105	-	120	°C
Storage Temperature	All	$T_{STG}$	-55	25	125	°C
Humidity (non-condensing)	Operating	All	5	-	95	%
	Non-operating	All	5	-	95	%

Note 1 - 1mA for 5s, slew rate = 1500V/10s

## Input Specifications

Table 2. Input Specifications:

Parameter	Conditions <sup>1</sup>	Symbol	Min	Typ	Max	Unit
Operating Input Voltage, DC	All	$V_{IN,DC}$	36	48	75	Vdc
Turn-on Voltage Threshold	$I_O = I_{O,max}$	$V_{IN,ON}$	31	34	36	Vdc
Turn-off Voltage Threshold	$I_O = I_{O,max}$	$V_{IN,OFF}$	30	33	35	Vdc
Inrush current transient rating	Power ON		-	-	1	A <sup>2</sup> s
Recommended Input Fuse <sup>1</sup>	Fast blow external fuse recommended		-	-	15	A
Input Reflected Ripple Current	5Hz to 20MHz: 12 $\mu$ H source impedance, $T_A = 25^{\circ}C$	$I_r$	-	20	30	mA <sub>PK-PK</sub>
Input voltage ripple rejection (ac)	120Hz		45	60	-	dB
Operating Efficiency	$T_A = 25^{\circ}C$ Air velocity: 300LFM $I_O = I_{O,max}$ $I_O = 50\%I_{O,max}$	$\eta$	90	92	-	%
			91	93	-	%

Note 1 -  $T_a = 25^{\circ}C$ , airflow rate = 400 LFM,  $V_{in} = 48V_{dc}$ , nominal  $V_{out}$  unless otherwise noted.

Note 2 - This power module is not internally fused. An input line fuse must always be used.

## Output Specifications

Table 3. Output Specifications:

Parameter	Conditions <sup>1</sup>	Symbol	Min	Typ	Max	Unit	
Factory Set Voltage	$V_{IN,DC} = 48V_{DC}$ $I_O = I_{O,max}$	$V_O$	11.76	12.00	12.24	Vdc	
Output Voltage Line Regulation	All	$\pm V_O$	-	-	15	mV	
Output Voltage Load Regulation	All	$\pm V_O$	-	-	25	mV	
Output Voltage Temperature Regulation	All	$\%V_O$	-	-	0.02	$\%/^{\circ}C$	
Output Voltage Trim Range	All	$\%V_O$	80	-	110	$\%V$	
Output Ripple, pk-pk	$T_A: 25^{\circ}C$ , Air velocity: 300LFM, $V_{in}: 48V$ , $V_{onom}, I_{onom}, 10\mu$ tantalum( $ESR \leq 100$ $m\Omega$ ) // $1\mu$ ceramic capacitor, 0 to 20MHz bandwidth	$V_O$	-	-	100	$mV_{PK-PK}$	
	Whole range, 0 to 20MHz bandwidth	$V_O$	-	-	150	$mV_{PK-PK}$	
Output Current	All	$I_O$	0	10	20	A	
Output DC current-limit inception <sup>2</sup>	All	$I_O$	22	-	28	A	
$V_O$ Load Capacitance <sup>3</sup>	All	$C_O$	470	2200	10000	$\mu F$	
$V_O$ Dynamic Response <sup>4</sup>	Peak Deviation Settling Time	$25\% I_{O,max}$ step from $50\% I_{O,max}$ , slew rate = $0.1A/\mu s$	$\pm V_O$ $T_s$	- -	- -	360 500	mV $\mu Sec$
	Peak Deviation Settling Time	$50\% I_{O,max}$ step from $50\% I_{O,max}$ , slew rate = $0.1A/\mu s$	$\pm V_O$ $T_s$	- -	- -	480 -	mV $\mu Sec$
	Peak Deviation Settling Time	$10\% I_{O,max}$ to $100\%$ $I_{O,max}$ , slew rate = $0.1A/\mu s$	$\pm V_O$ $T_s$	- -	- -	600 -	mV $\mu Sec$
Turn-on transient	Turn-on delay time	$I_O = I_{O,max}$ $V_O$ from 10% to 90%	$T_{turn-on}$	-	-	20	mS
	Output voltage overshoot	$T_A = 25^{\circ}C$ $I_O = I_{O,max}$	$\%V_O$	-	-	5	%

Note 1 -  $T_A = 25^{\circ}C$ , airflow rate = 400 LFM,  $V_{in} = 48V_{dc}$ , nominal  $V_{out}$  unless otherwise noted.

Note 2 - Hiccup: auto-restart when over-current condition is removed.

Note 3 -  $T_A: 25^{\circ}C$ ,  $V_{in}: 48V$ ,  $30000\mu F$  can start-up.

Note 4 - The external capacitor should be  $10\mu$  tantalum( $ESR \leq 100 m\Omega$ ) //  $1\mu$  ceramic capacitor

## Output Specifications

Table 3. Output Specifications, con't:

Parameter		Conditions <sup>1</sup>	Symbol	Min	Typ	Max	Unit
Isolation Capacitance		-	-	-	-	-	PF
Isolation Resistance		-	-	10	-	-	MΩ
Switching frequency		All	f <sub>sw</sub>	-	250	-	KHz
Enable pin voltage	Logic Low	All	-	-0.7	-	1.2	Vdc
	Logic High	All	-	3.5	-	12	Vdc
Enable pin current	Logic Low	All	-	-	-	1.0	mA
	Logic High	All	-	-	-	-	uA
Output over-voltage protection <sup>5</sup>	Static	All	V <sub>O</sub>	14	-	18	Vdc
	Dynamic	All	V <sub>O</sub>	14	-	18	Vdc
Output over-temperature protection <sup>6</sup>	Open frame	T		85	-	105	°C
	Baseplate			85	-	100	
Over-temperature hysteresis		All	T	5	-	-	°C
MTBF		Vin: 48V, 100%Load, Board T <sub>c</sub> =25°C		-	2	-	10 <sup>6</sup> h
Vibration(Sine wave)		Vibration level: 3.5mm (2 ~ 9Hz), 10m/s <sup>2</sup> (9 ~ 200HZ), 15m/s <sup>2</sup> (200 ~ 500Hz) Directions and time: 3 axis (X, Y, Z), 30 minutes each Sweep velocity: 1 oct / min					
Shock (Half-sine wave)		Peak acceleration: 300m/s <sup>2</sup> Duration time: 6ms Continuous shock 3 times at each of 6 directions ( ± X, ± Y, ± Z)					

Note 5 - Hiccup: auto-restart when over-voltage condition is removed.

Note 6 - Auto recovery.

## AVE240-48S12 Performance Curves

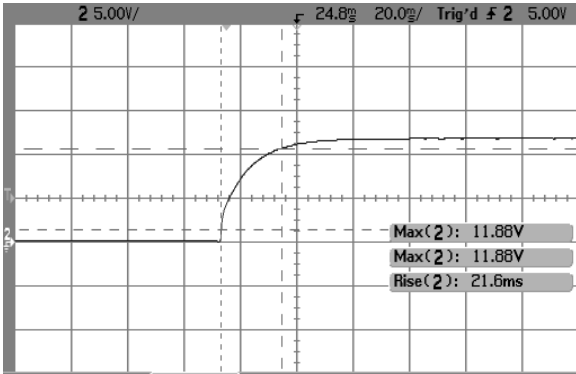


Figure 1: AVE240-48S12-4 Output Voltage Startup Characteristic

Ch 1: Vo (5V/div)

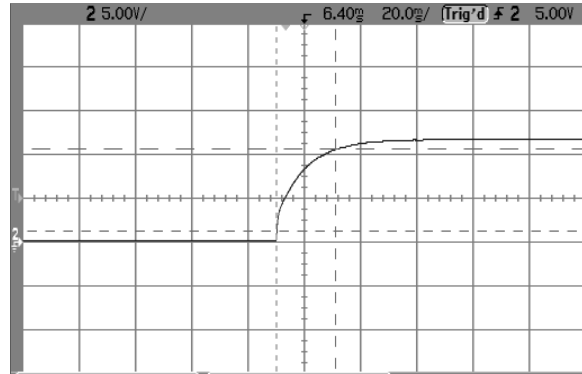


Figure 2: AVE240-48S12-4 Remote ON Waveform (20ms/div)

Ch 1: Vo (5V/div)

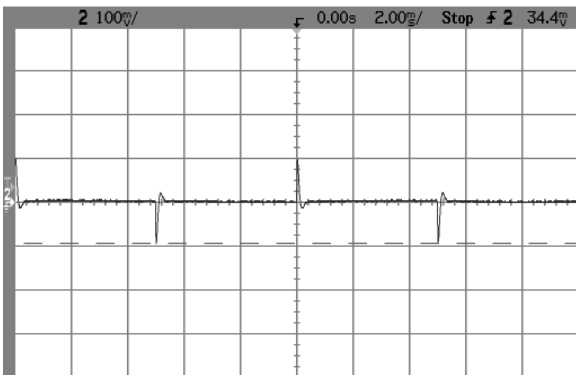


Figure 3: AVE240-48S12-4 Transient Response (2mS/div)  
 25%  $I_{O,max}$  step from 50%  $I_{O,max}$ , 0.1A/uS slew rate

Ch 1: Vo (100mV/div)

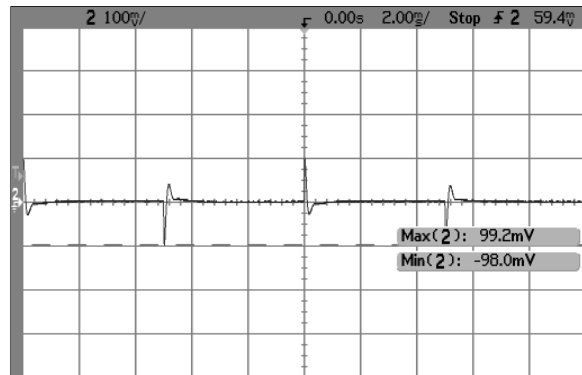


Figure 4: AVE240-48S12-4 Transient Response (2mS/div)  
 50%  $I_{O,max}$  step from 75%  $I_{O,max}$ , 0.1A/uS slew rate

Ch 1: Vo (100mV/div)

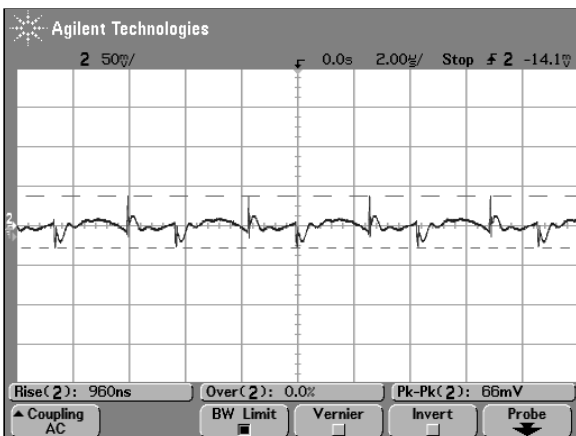


Figure 5: AVE240-48S12-4 Ripple and Noise measurement  
 $V_{in} = 48V_{dc}$ ,  $V_{out} = 12V$ ,  $I_{out} = 20A$ , Air Velocity=300LFM

Ch 1: Vo (50mV/div)

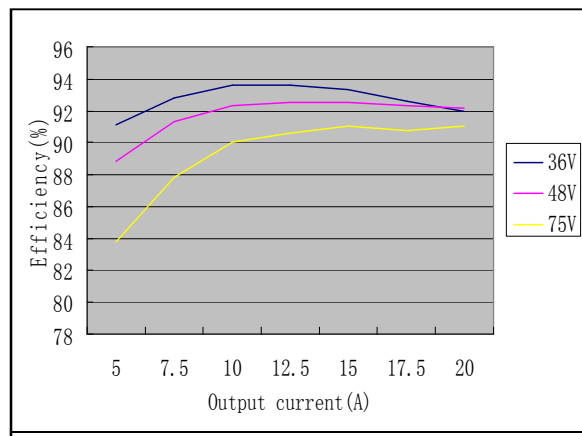


Figure 6: AVE240-48S12 Efficiency Curves @ 25°C

Loading:  $I_o = 25\%$  increment to 20A

**Protection Function Specification**

**Over Voltage Protection (OVP )**

The output over-voltage protection consists of circuitry that monitors the voltage on the output terminals. If the voltage on the output terminals exceeds the over voltage protection threshold, then the module will work on hiccup mode. When the over-voltage condition is removed, the converter will automatically restart.

The protection mechanism is such that the unit can continue in this condition until the fault is cleared

Parameter	Min	Nom	Max	Unit
V <sub>O</sub> Output Overvoltage	14	/	18	V

**Over Current Protection (OCP)**

AVE240-48S12 DC/DC converter feature foldback current limiting as part of their Over-current Protection (OCP) circuits. When output current exceeds 110 to 140% of rated current, such as during a short circuit condition, the module will work on intermittent mode, also can tolerate short circuit conditions indefinitely. When the over-current condition is removed, the converter will automatically restart.

Parameter	Min	Nom	Max	Unit
V <sub>O</sub> Output Overcurrent	110	/	140	%A

**Over-Temperature Protection (OTP)**

These modules feature an over-temperature protection circuit to safeguard against thermal damage. The module will work in intermittent mode when the maximum device reference temperature is exceeded. When the over-temperature condition is removed, the converter will automatically restart.

**Input Reverse Voltage Protection**

Under installation and cabling conditions where reverse polarity across the input may occur, reverse polarity protection is recommended. Protection can easily be provided as shown in Figure 7. In both cases the diode used is rated for 15A/100V. Placing the diode across the inputs rather than in-line with the input offers an advantage in that the diode only conducts in a reverse polarity condition, which increases circuit efficiency and thermal performance.

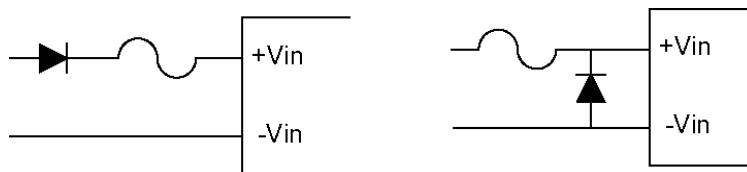
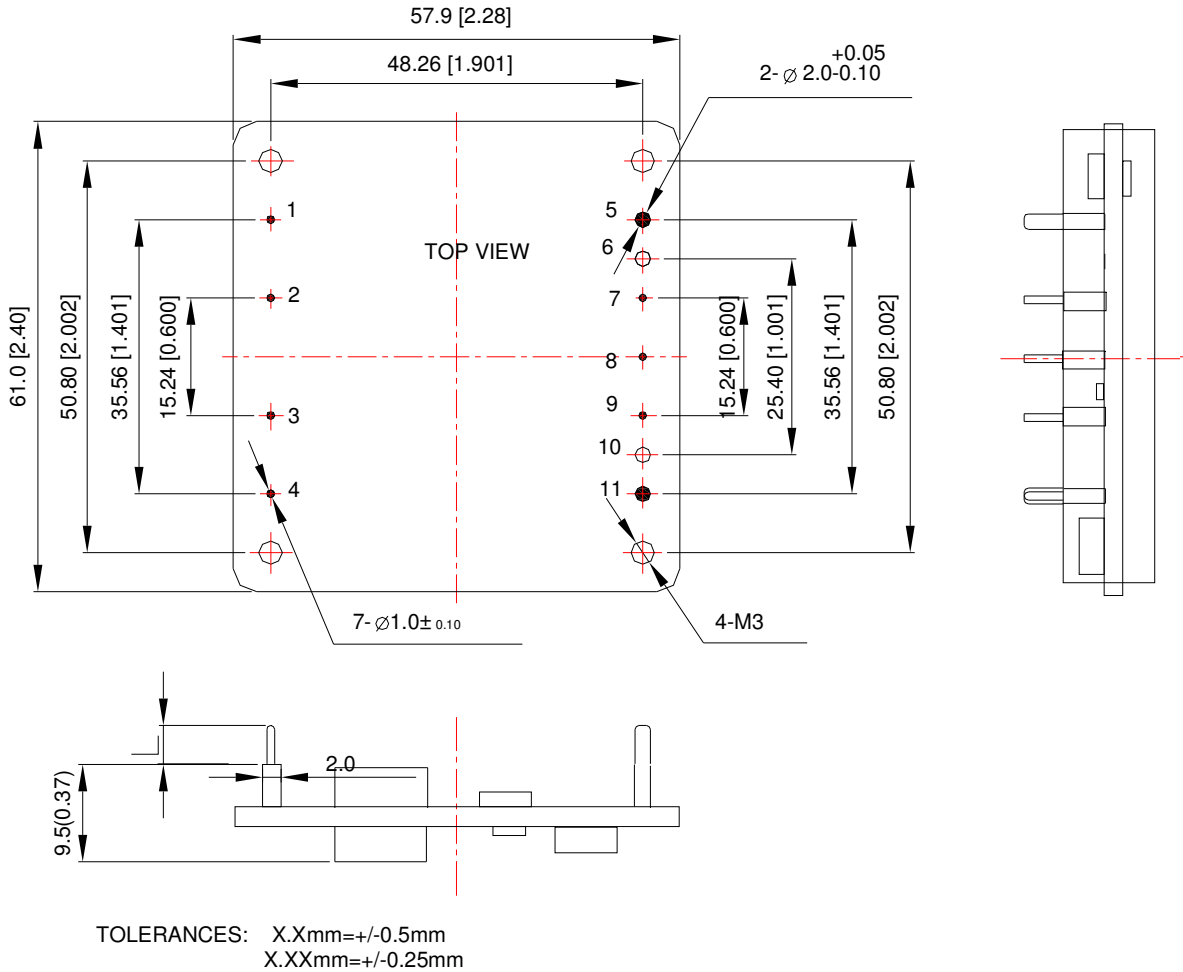


Figure 7 Reverse polarity protection circuit



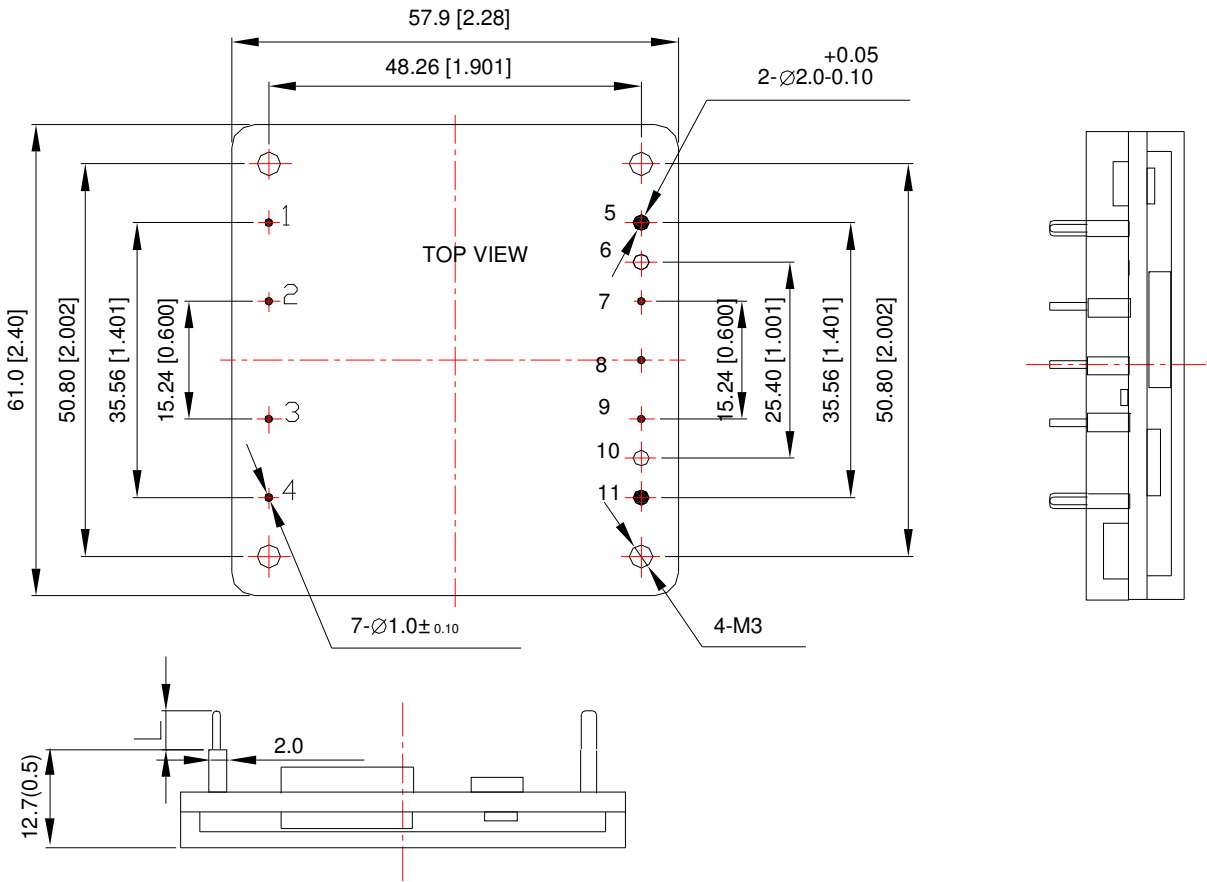
## Mechanical Specifications

### Mechanical Outlines — Open frame Module



## Mechanical Specifications

### Mechanical Outlines — Baseplate Module



TOLERANCES: X.Xmm= $\pm 0.5$ mm  
 X.XXmm= $\pm 0.25$ mm

## Pin Length options

Device code suffix	L
-4	4.8mm+/-0.5 mm
-6	3.8mm+/-0.5 mm
-8	2.8mm+0.5/-0.3 mm
None	5.8mm+/-0.5 mm

## Pin Designations

Pin No	Name	Function
1	Vin+	Positive input voltage
2	CNT	Remote ON/OFF control
3	NC	Not Connected
4	Vin-	Negative input voltage
5	Vo+	Positive output voltage
6	NC	Not Connected
7	+SENSE	Positive remote sense
8	TRIM	Output voltage trim
9	-SENSE	Negative remote sense
10	NC	Not Connected
11	Vo-	Negative output voltage

## Safety Certifications

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., UL60950, CSA C22.2, and EN60950. AVE240-48S12 input-to-output isolation is a basic insulation. The DC/DC power module should be installed in end-use equipment, in compliance with the requirements of the ultimate application, and is intended to be supplied by an isolated secondary circuit. When the supply to the DC/DC power module meets all the requirements for SELV (<60Vdc), the output is considered to remain within SELV limits (level 3). If connected to a 60Vdc power system, double or reinforced insulation must be provided in the power supply that isolates the input from any hazardous voltages, including the ac mains. One input pin and one output pin are to be grounded or both the input and output pins are to be kept floating. Single fault testing in the power supply must be performed in combination with the DC/DC power module to demonstrate that the output meets the requirement for SELV. The input pins of the module are not operator accessible.

Note: Do not ground either of the input pins of the module, without grounding one of the output pins. This may allow a non-SELV voltage to appear between the output pin and ground. The circuit cannot withstand transient over-voltage.

Table 4. Safety Certifications for the AVE240-48S12 series power supply system

Document	File #	Description
UL60950/CSA C22.2		US and Canada Requirements
EN60950		European Requirements

## Operating Temperature

The AVE240 series power supplies will start and operate within stated specifications at an ambient temperature from -40 °C to 55 °C under all load conditions. The storage temperature is -55 °C to 125 °C.

## Thermal Considerations – Open-frame module

AVE240-48S12 modules have ultra high efficiency at full load. With less heat dissipation and temperature-resistant components such as ceramic capacitors, these modules exhibit good behavior during pro-longed exposure to high temperatures. Maintaining the operating board temperature within the specified range help keep internal component temperatures within their specifications which in turn help keep MTBF from falling below the specified rating. Proper cooling of the power modules is also necessary for reliable and consistent operation. Measuring the board temperature of the module is shown in Figure 8 (with baseplate) and Figure 9 (without baseplate) can verify the proper cooling.

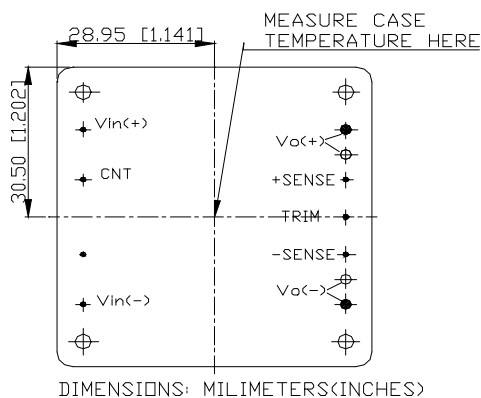


Figure 8 Temperature with baseplate

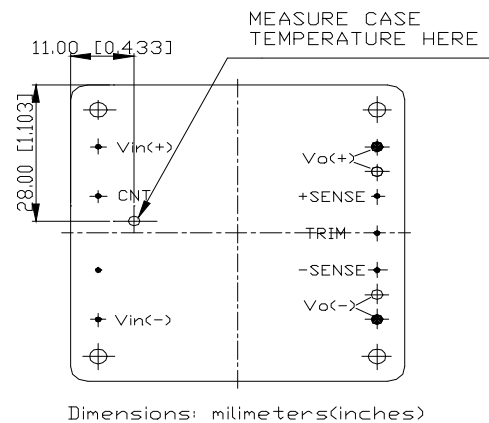


Figure 9 Temperature with open frame

The module should work under 85 °C ambient for the reliability of operation and the board temperature must not exceed 105 °C (open frame) or 100 °C (with baseplate) while operating in the final system configuration. The measurement can be made with a surface probe after the module has reached thermal equilibrium. No heatsink is mounted, make the measurement as close as possible to the indicated position. It makes the assumption that the final system configuration exists and can be used for a test environment. Note that the board temperature of module must always be checked in the final system configuration to verify proper operational due to the variation in test conditions. Thermal management acts to transfer the heat dissipated by the module to the surrounding environment. The amount of power dissipated by the module as heat (PD) is got by the equation:

$$PD = PI - PO$$

Where PI is input power; PO is output power; PD is dissipated power.

Also, module efficiency ( $\eta$ ) is defined as the following equation:  $\eta = PO / PI$

If eliminating the input power term, from two above equations can yield the equation below:

$$PD = PO (1 - \eta) / \eta$$

The module power dissipation then can be calculated through the equation.

Because each power module output voltage has a different power dissipation curve, a plot of power dissipation versus output current over three different line voltages is given in the following figures.

### Module Derating

From the experimental set up shown in Figure 10, the derating curves as Figure 12 can be drawn. Note that the Printed Wiring Board (PWB) and the module must be mounted vertically. The Passage has a rectangular cross-section. The clearance between the facing PWB and the top of the module is kept 13 mm (0.5 in.) constantly.

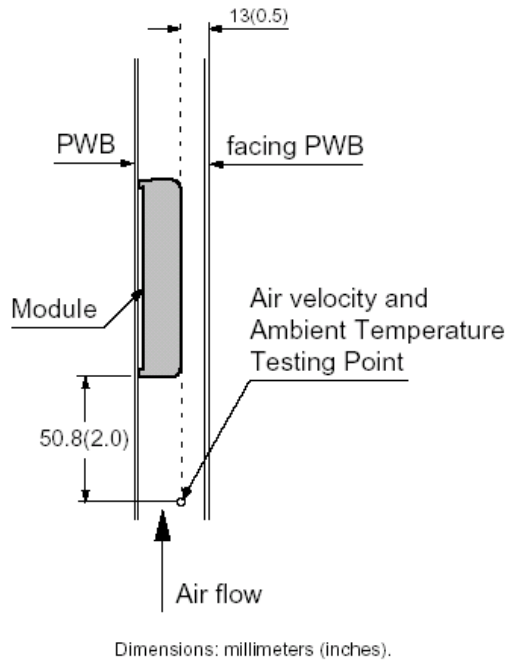


Figure 10 Experiment setup

**Convection Without Heatsinks**

Increasing the airflow over the module can enhance heat transfer. Figure 11 and Figure 12 shows the change of the module output current with the change of ambient temperature. In the test, the airflow was created with externally adjustable fans. The appropriate airflow for a given operating condition can be determined through this figure.

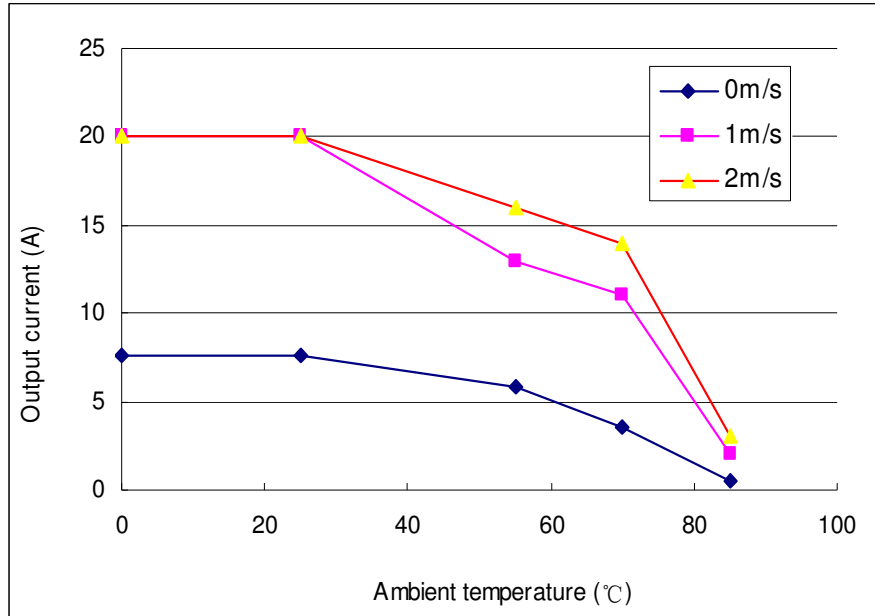


Figure 11 Forced convection power derating with open frame  
 Airflow direction from Vin+ to Vin-: Vin=48V

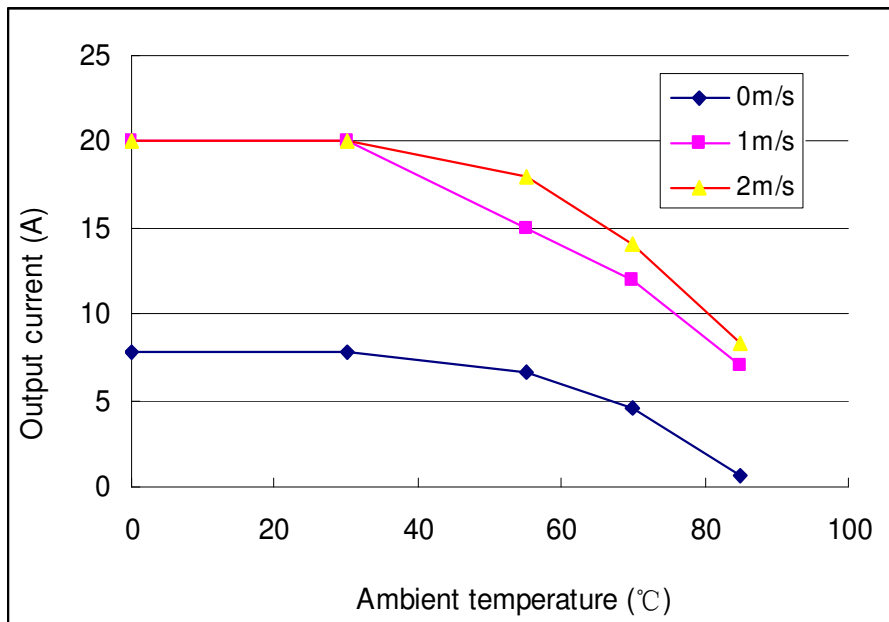


Figure 12 Forced convection power derating with baseplate  
 Airflow direction from Vin+ to Vin-: Vin=48V

**Heatsink Configuration**

Several standard heatsinks available for the AVE240-48S12 are shown in Figure 13 to 15.

The heatsinks mounted to the top surface of the module with screws torqued to 0.56 N-m (5 in.-lb). A thermally conductive dry pad or thermal grease is placed between the case and the heatsink to minimize contact resistance (typically 0.1 °C/W to 0.3 °C /W) and temperature differential.

Nomenclature for heatsink configurations is as follows:

WDxyyy40

x = fin orientation: longitudinal (L) or transverse (T)

yyy = heatsink height (in 100ths of inch)

For example, WDT5040 is a heatsink that is transverse mounted for a 61mm × 57.9mm (2.4in × 2.28in) module with a heatsink height of 0.5 in.

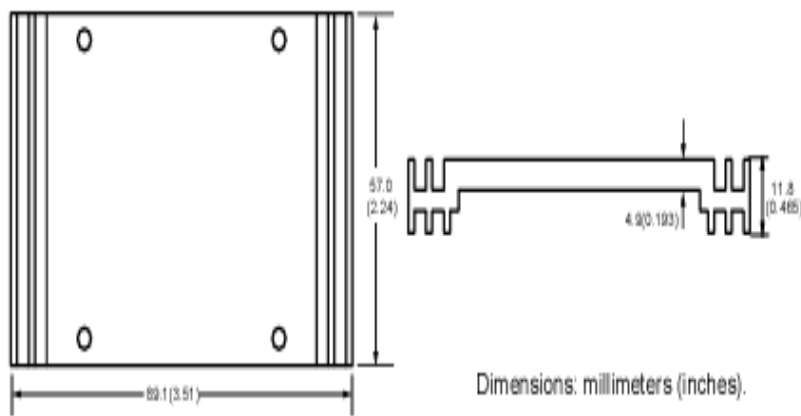


Figure 13 Non-standard heatsink

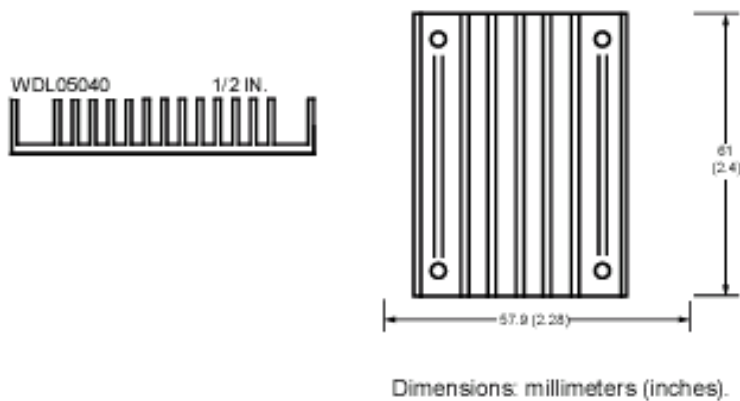


Figure 14 Longitudinal fins heatsink



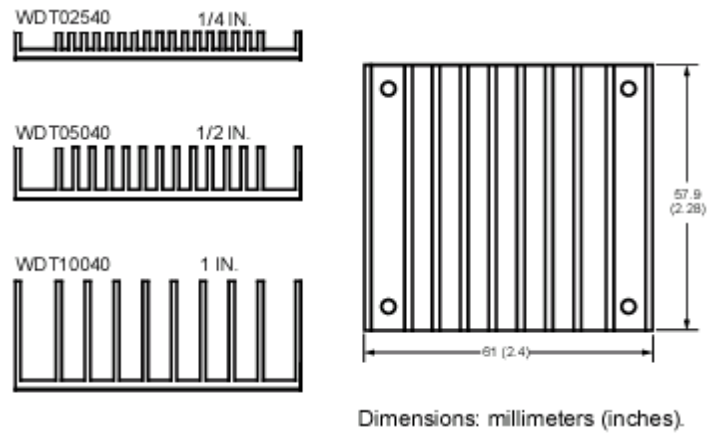


Figure 15 Transverse fins heatsink

### Heatsink Mounting

A crucial part of the thermal design strategy is the thermal interface between the baseplate of the module and the heatsink. Inadequate measures taken will quickly negate any other attempts to control the baseplate temperature. For example, using a conventional dry insulator can result in a case-heatsink thermal impedance of  $>0.5 \text{ }^{\circ}\text{C/W}$ , while use one of the recommended interface methods (using silicon grease or thermal pads) can result in a case-heatsink thermal impedance around  $0.1 \text{ }^{\circ}\text{C/W}$

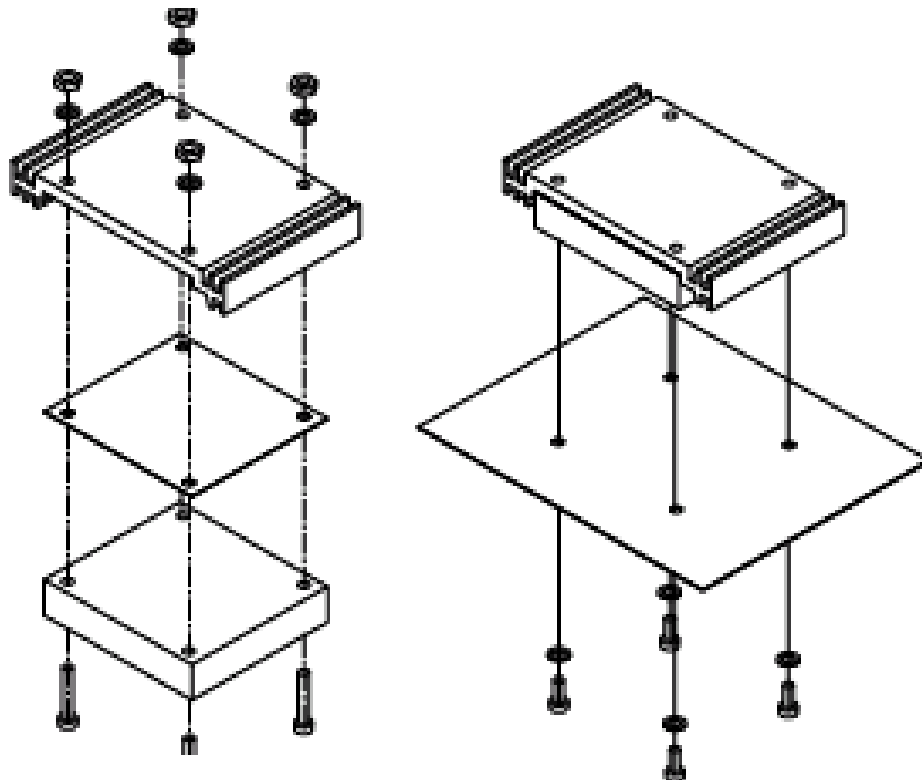


Figure 16 Heatsink mounting

## Application Notes

### Typical Application

Below is the typical application of the AVE240-48S12 series power supply.

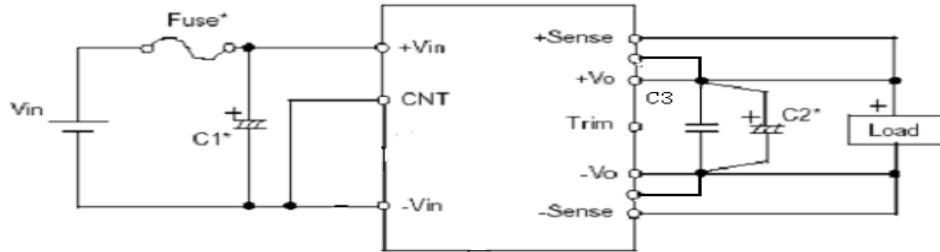


Figure 17 Typical application

F1: Fuse\*: Use external fuse (fast blow type) for each unit. For 12V output: 10A (Pout=240W)

C1: Recommended input capacitor  $C1 \geq 100\mu\text{F}/100\text{V}$  electrolytic or ceramic type capacitor.

C2: Recommended  $-5\text{ }^\circ\text{C} \sim 100\text{ }^\circ\text{C}$  uses:  $2,200\mu\text{F}/10\text{V}$  (electrolytic capacitor);  $-40\text{ }^\circ\text{C} \sim -5\text{ }^\circ\text{C}$ : for this temperature range, use  $2,200\mu\text{F}/50\text{V}$  electrolytic capacitor and  $220\mu\text{F}/10\text{V}$  tantalum capacitor.

C3: Recommended  $1\mu\text{F}/10\text{V}$

Fusing: The AVE240-48S12 power module has no internal fuse. An external fuse must always be employed! To meet international safety requirements, a 250 Volt rated fuse should be used. If one of the input lines is connected to chassis ground, then the fuse must be placed in the other input line.

## CNT Function

Two CNT logic options are available. The CNT logic, CNT voltage and the module working state are as the following table

	L	H	OPEN
N	ON	OFF	OFF
P	OFF	ON	ON

N--- means "Negative Logic"

P--- means "Positive Logic"

L--- means "Low Voltage",  $-0.7V \leq L \leq 1.2V$

H--- means "High Voltage",  $3.5V \leq H \leq 12V$

ON--- means "Module is on", OFF--- means "Module is off"

Open--- means "CNT pin is left open "

Note: when CNT is left open,  $V_{CNT}$  may reach 6V.

Figure 18 shows a few simple CNT circuits.

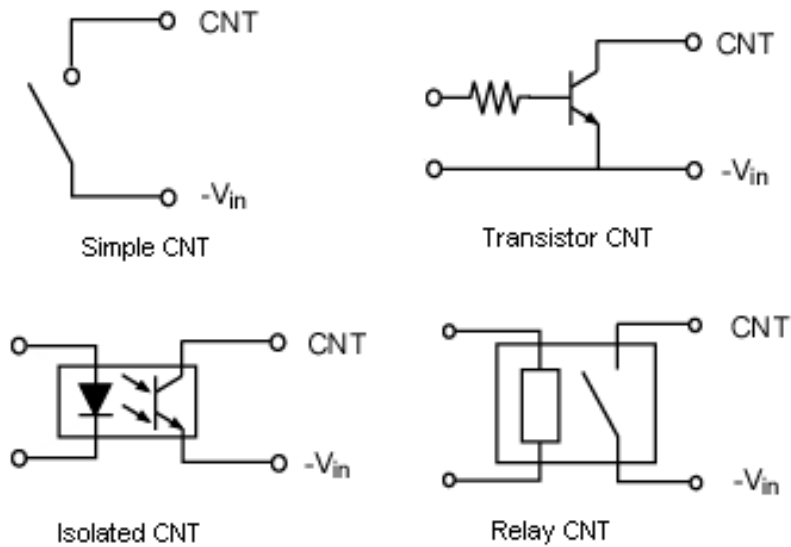


Figure 18 CNT circuit

## Trim Characteristics

The +Vo output voltage of AVE240-48S12 can be trimmed using the trim pin provided. Applying a resistor to the trim pin through a voltage divider from the output will cause the +Vo output to increase by up to 10% or decrease by up to 20%. Trimming up by more than 10% of the nominal output may activate the OVP circuit or damage the converter. Trimming down more than 20% can cause the converter to regulate improperly. If the trim pin is not needed, it should be left open.

### Trim up

With an external resistor connected between the TRIM and +SENSE pins, the output voltage set point increases (see Figure 19).

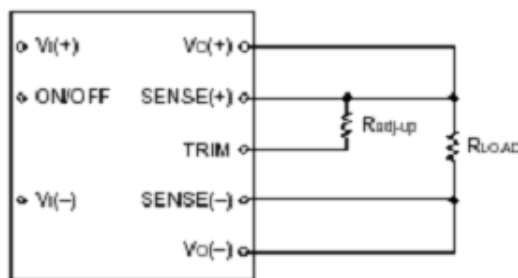


Figure 19 Trim up circuit

The following equation determines the required external-resistor value to obtain a percentage output voltage change of %.

$$R_{adj-up} = \left( \frac{V_{o,nom} * (100 + \Delta\%)}{1.225 * \Delta\%} - \frac{(100 + 2 * \Delta\%)}{\Delta\%} \right) K\Omega$$

Where,

$$\Delta\% = \left| \frac{V_{o,nom} - V_{desired}}{V_{o,nom}} \right| \times 100$$

$V_{desired}$  = Desired output voltage set point (V).

$V_{trim}$  tolerance less than  $\pm 2\%$ ,

$R_{adj}$  tolerance is  $\pm 1\%$

## Trim down

With an external resistor between the TRIM and -SENSE pins, the output voltage set point decreases (see Figure 20).

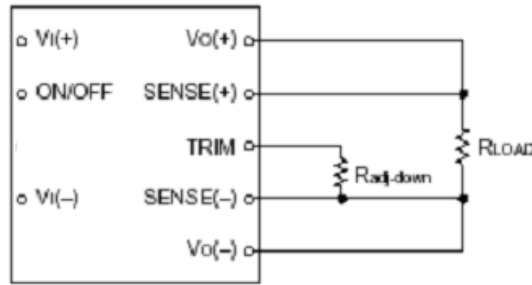


Figure 20 Trim down circuit

The following equation determines the required external-resistor value to obtain a percentage output voltage change of %.

$$R_{adj-down} = \left( \frac{100}{\Delta\%} - 2 \right) K\Omega$$

Where,

$$\Delta\% = \left| \frac{V_{o,nom} - V_{desired}}{V_{o,nom}} \right| \times 100$$

$V_{desired}$  = Desired output voltage set point (V).

$V_{trim}$  tolerance less than  $\pm 2\%$ ,

$R_{adj}$  tolerance is  $\pm 1\%$

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim.

Note that at elevated output voltages the maximum power rating of the module remains the same, and the output current capability will decrease correspondingly

## Remote Sense

AVE240-48S12 converter can remotely sense both lines of its output which moves the effective output voltage regulation point from the output terminals of the unit to the point of connection of the remote sense pins. This feature automatically adjusts the real output voltage of AVE240-48S12 in order to compensate for voltage drops in distribution and maintain a regulated voltage at the point of load.

When the converter is supporting loads far away, or is used with undersized cabling, significant voltage drop can occur at the load. The best defense against such drops is to locate the load close to the converter and to ensure adequately sized cabling is used. When this is not possible, the converter can compensate for a drop of up to  $10\%V_o$ , through use of the sense leads.

When used, the + SENSE and - SENSE leads should be connected from the converter to the point of load as shown in Figure 21, using twisted pair wire, or parallel pattern to reduce noise effect. The converter will then regulate its output voltage at the point where the leads are connected. Care should be taken not to reverse the sense leads. If reversed, the converter will trigger OVP protection and turn off. When not used, the +SENSE lead must be connected with  $V_{o+}$ , and -SENSE with  $V_{o-}$ . Although the output voltage can be increased by both the remote sense and trim, the maximum increase for the output voltage is not the sum of both.

The maximum increase is the larger of either the remote sense or the trim.

Note that at elevated output voltages the maximum power rating of the module remains the same, and the output current capability will decrease correspondingly.

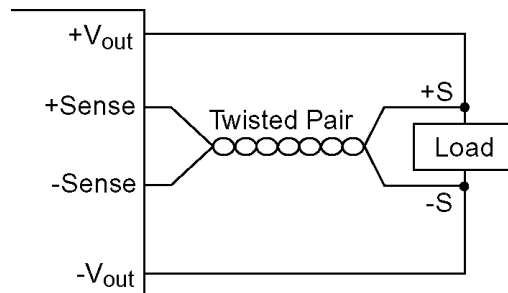


Figure 21 Sense connection

## Output Capacitance

High output current transient rate of change (high  $di/dt$ ) loads may require high values of output capacitance to supply the instantaneous energy requirement to the load. To minimize the output voltage transient drop during this transient, low Equivalent Series Resistance (ESR) capacitors may be required, since a high ESR will produce a correspondingly higher voltage drop during the current transient.

When the load is sensitive to ripple and noise, an output filter can be added to minimize the effects. A simple output filter to reduce output ripple and noise can be made by connecting a capacitor  $C_1$  across the output as shown in Figure 22. The recommended value for the output capacitor  $C_1$  is  $2200\mu\text{F}$ .

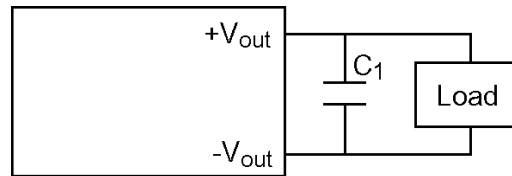


Figure 22 Output ripple filter

Extra care should be taken when long leads or traces are used to provide power to the load. Long lead lengths increase the chance for noise to appear on the lines. Under these conditions  $C_2$  can be added across the load, with a  $1\mu\text{F}$  ceramic capacitor  $C_2$  in parallel generally as shown in Figure 23.

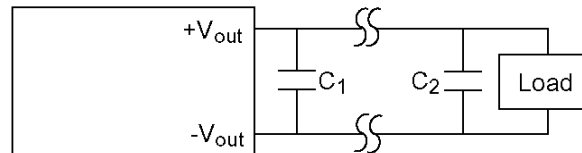


Figure 23 Output ripple filter for a distant load

## Decoupling

The converter does not always create noise on the power distribution system. High-speed analog or digital loads with dynamic power demands can cause noise to cross the power inductor back onto the input lines. Noise can be reduced by decoupling the load. In most cases, connecting a  $10\mu\text{F}$  ceramic capacitor in parallel with a  $0.1\mu\text{F}$  ceramic capacitor across the load will decouple it. The capacitors should be connected as close to the load as possible.

## Ground Loops

Ground loops occur when different circuits are given multiple paths to common or earth ground, as shown in Figure 24. Multiple ground points can slightly different potential and cause current flow through the circuit from one point to another. This can result in additional noise in all the circuits. To eliminate the problem, circuits should be designed with a single ground connection as shown in Figure 25.

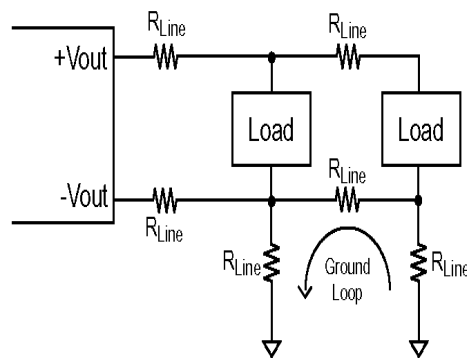


Figure 24 Ground loops

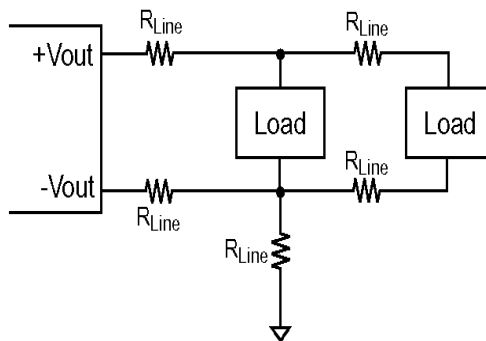


Figure 25 Single point ground



## Weight

The AVE240-48S12 series weight is 103g with baseplate and 72g without baseplate.

## Installation

Although AVE240-48S12 converters can be mounted in any orientation, free air-flowing must be taken. Normally power components are always put at the end of the airflow path or have the separate airflow paths. This can keep other system equipment cooler and increase component life spans.

## Soldering

AVE240-48S12 converter is compatible with standard wave soldering techniques. When wave soldering, the converter pins should be preheated for 20-30 seconds at 110 °C, and wave soldered at 260 °C for less than 10 seconds.

When hand soldering, the iron temperature should be maintained at 425 °C and applied to the converter pins for less than 5 seconds. Longer exposure can cause internal damage to the converter. Cleaning can be performed with cleaning solvent IPA or with water.

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